

# Víctor Hugo Palacios Rivera

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## CONTACT

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## QUALIFICATIONS

- Excellent problem analysis and problem solving skills.
- Solid understanding of Algorithms, Data Structures and Object Oriented Programming.
- Ability to quickly pick up and own new concepts to contribute with innovative solution.
- C, Java, C# and Python programming background.
- Oracle Certified Professional, Java SE 6 Programmer.
- Expertise in J2EE Technologies, Java WebServices and XML development.
- Web Development using HTML, JavaScript, JSON, Ajax.
- Database development with DB2, Mysql, Postgresql and Hibernate technologies.
- Moderate-Advanced Unix/Linux knowledge, including Kernel customization and Bash scripting.
- Basic/Intermediate knowledge of DSP, basic implementations using Matlab/Simulink/HDLCoder
- Moderate FPGA design understanding with Altera Arria 10/Quartus Prime flow; familiar with the Intel FPGA IP portfolio (Avalon MM, ST, SPI & i2c cores, Frame Buffer II, etc).
- Moderate understanding of electronic circuit theory.

## WORK EXPERIENCE

Intel Tecnología de Mexico  
Software/Firmware Engineer

Jun 2016 — Present

- Part of the Systems Prototyping Lab research team, working on the ADAS research agenda for Intel Labs.
- Work activities include development of research platforms, PoC delivery, technology transfer to business units and IP generation.
- Design, develop, testing and integration of low level software (C/C++) and IP cores (System Verilog) for different Intel Arria 10 FPGA custom designs.

Intel Tecnología de Mexico  
Graduate Intern Technical

Oct 2015 — Jun 2016

- Intel's GDC IoT Lab Coordinator.
- Conduct IoT training for universities, schools and laboratories.
- Provide technological guidance to startups and small enterprises.
- Develop innovative proof of concepts using Intel's embedded platforms (Edison, Galileo).

TCS Limited America  
Computer Systems Engineer

Aug 2012 — Jun 2014

- Part of the on-site (San Antonio, TX) document services team, designing, implementing and supporting internal systems for USAA, a large military bank and insurance company.
- First opportunity to design a complex system from scratch, supervised by the lead architect of the project.
- Production monitoring and support for several applications/systems owned by the team, dealing with urgent incidents and money-critical services.

- Offshore (Chennai, Hyderabad) resource coordination for both development and maintenance activities.

## Tata Consultancy Services

Sept 2010 — Aug 2012

### Java Developer

- Part of the nearshore (Guadalajara) bank business services team, developing back-end systems for USAA.
- Using the Java J2EE platform and web services technologies, we provided the necessary interfaces to access information from an external credit score vendor.

## Sistema de Universidad Virtual

Dec 2009 — Sept 2010

### IT Support

- Telephony and network support.
- General IT support for the main offices.

## PATENTS & PUBLICATIONS

Victor Palacios Rivera , Laura Rumbel , Ignacio Alvarez, Autonomous HMI Made Easy: Prototyping Reactive In-cabin Aware HMIs, Adjunct Proceedings of the 8th International Conference on Automotive User Interfaces and Interactive Vehicular Applications, October 24-26, 2016, Ann Arbor, MI, USA [doi>10.1145/3004323.3004353]

Emotional Adaptive driving policies for Automated Driving Vehicles. Inventors: Jennifer Healey (San Jose, CA), Victor Palacios Rivera (Guadalajara), Ignacio Alvarez (Portland, OR). Publication number: 20190049957. Filed: Mar 30, 2018. Application Number: 15/941,303

Heterogeneous Compute Architecture Hardware/Software Co-design for autonomous driving. Inventors: Ignacio Alvarez (Portland, OR), Patrick Mead (Santa Clara, CA), Carlos Ornelas (Guadalajara), Daniel Lake (Hillsboro, OR), Miryam Lomeli Barajas (Tlaquepaque), Victor Palacios Rivera (Guadalajara), Yassir Mosleh (Wilsonville, OR), David Arditti Ilitzky (Zapopan), John Tell (Tigard, OR), Paul H. Dormitzer (Santa Clara, CA). Publication number: 20190050522. Filed: Oct 9, 2018. Application Number: 16/155,039

Simulated Vehicle Operation Modeling with Real Vehicle Profiles. Inventors: Ignacio Alvarez (Portland, OR), Victor Palacios Rivera (Guadalajara). Publication number: 20190050520. Filed: Jan 12, 2018. Application Number: 15/869,313

Rogue Vehicle Detection and Avoidance. Inventors: Ignacio Alvarez (Portland, OR), Victor Palacios Rivera (Guadalajara), Daniel Lake (Hillsboro, OR), David Arditti Ilitzky (Zapopan), Rafael de la Guardia Gonzalez (Guadalajara), Patrick Mead (Portland, OR). Publication number: 20190051179. Filed: Aug 23, 2018. Application Number: 16/111,041.

## EDUCATION

### Master of Science in Computer Science

Sept 2014 — Present

Centro de Investigación y de Estudios Avanzados del Instituto Politécnico Nacional, Guadalajara.

- Handity project, a project aimed at giving haptic feedback to a user immersed in a VR environment created with the Unity3D engine.
- CityFARM IoT platform, a project focused in providing IoT/cloud capabilities to the urban agriculture developments in Guadalajara.
- Thesis focused on service composition and coordination for Multi-Agent Systems running on embedded platforms.

**Bachelor of Science in Computer  
Science**

**Aug 2005 — Sept 2010**

Centro Universitario de Ciencias Exactas e Ingenierías. Guadalajara. México.

- Major/Specialization in software development.
- CREES award received for best GPA score (97.07) in UdeG Computer Engineer class of 2010.

**Student Interchange Program**

**Sept 2008 — Aug 2009**

Universität Bayreuth. Bayreuth, Germany.

- Foreign language studies (German, Italian).
- Software development focusing on database managing.

**Technical degree in Computer  
Science**

**Sept 2001 — Aug 2004**

Centro de Enseñanza Técnica Industrial. Plantel Colomos, Guadalajara. México.

- Software and hardware technician.
- Degree focused more on the electronics part of computer science.
- Designed and implemented a minimal circuit for the 80C52 microprocessor.

**LANGUAGES**

Spanish - Native language.

English - Full professional proficiency .

German - Conversational level/B2.